09/938,708   Liu

EAST SEARCH

1/30/05

L#	Hits	Search String		Databases
L1	131	state space same (reduce$5 and model$5)		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB
L2	78	state space same (reduce$5 and model$5)		USPAT; EPO; JPO; DERWENT; IBM_TDB
L3	8	state space and (reduce$5 same model$5) and transitiv$5		USPAT; EPO; JPO; DERWENT; IBM_TDB

Results of search set L2:

US 3662343 A	CREDIT CARD AUTOMATIC CURRENCY DISPENSER	19720509	235/381
US 6826521 B1	System and methodology and adaptive, linear model predictive control based on rigorous, nonlinear process model	20041130	703/12
US 6795795 B2	Probabilistic map for a building	20040921	702/181
US 6789237 B1	Efficient model order reduction via multi-point moment matching	20040907	716/4
US 6778968 B1	Method and system for facilitating opportunistic transactions using auto-probes	20040817	705/37
US 6757579 B1	Kalman filter state estimation for a manufacturing system	20040629	700/108
US 6751582 B1	Method and system for enhanced design validation through trace tailoring	20040615	703/14
US 6748170 B1	Method for optimizing a mostly optical network	20040608	398/47
US 6728939 B2	Method of circuit verification in digital design	20040427	716/5
US 6715107 B2	Method and system for reducing the computation tree to include only model behaviors defined succinctly by a user via regular expressions as being of interest	20040330	714/37
US 6691073 B1	Adaptive state space signal separation, discrimination and recovery	20040210	702/190
US 6665848 B2	Time-memory tradeoff control in counterexample production	20031216	716/5
US 6643827 B1	Symbolic model checking with dynamic model pruning	20031104	716/2
US 6609036 B1	Surveillance system and method having parameter estimation and operating mode partitioning	20030819	700/30
US 6587751 B2	Robot device and method for controlling the robot's emotions	20030701	700/245
US 6567959 B2	Method and device for verification of VLSI designs	20030520	716/5
US 6564176 B2	Signal and pattern detection or classification by estimation of continuous dynamical models	20030513	702/189
US 6508102 B1	Near real-time friction estimation for pre-emptive vehicle control	20030121	73/8
US 6473851 B1	System for combining plurality of input control policies to provide a compositional output control policy	20021029	713/1
US 6462962 B1	Lossless switching DC-to-DC converter	20021008	363/16

Patent	Title	Date	Class
US 6453308 B1	Non-linear dynamic predictive device	20020917	706/21
US 6445978 B1	Robot device and method for controlling the same	20020903	700/245
US 6408262 B1	Method and an apparatus for analyzing a state based system model	20020618	703/2
US 6400579 B2	Lossless switching DC to DC converter with DC transformer	20020604	363/16
US 6388896 B1	Lossless switching converter with DC transformer	20020514	363/16
US 6385765 B1	Specification and verification for concurrent systems with graphical and textual editors	20020507	717/100
US 6366841 B1	Damping force control device and method	20020402	701/37
US 6349272 B1	Method and system for modeling time-varying systems and non-linear systems	20020219	703/2
US 6343372 B1	Methods and apparatus for generating a verified algorithm for transforming a program from a first form to a second form	20020129	717/136
US 6321184 B1	Reduction of arbitrary L1-L2 circuits for enhanced verification	20011120	703/15
US 6320113 B1	System for enhancing the sound of an acoustic instrument	20011120	84/738
US 6317662 B1	Stable and verifiable state estimation methods and systems with spacecraft applications	20011113	701/13
US 6304460 B1	Switching DC-to-DC converter utilizing a soft switching technique	20011016	363/16
US 6295515 B1	Static partial order reduction	20010925	703/13
US 6289255 B1	Method for computer-aided design of a product or process	20010911	700/97
US 6209120 B1	Verifying hardware in its software context and vice-versa	20010327	716/5
US 6178394 B1	Protocol checking for concurrent systems	20010123	703/17
US 6158267 A	Method and apparatus for modeling the dynamic effects on a gyroscope	20001212	73/1.77
US 6102959 A	Verification tool computation reduction	20000815	703/2
US 6101058 A	Method of implementing a linear discrete-time state-space servo control system on a fixed-point digital signal processor in a disc drive	20000808	360/69
US 6049662 A	System and method for model size reduction of an integrated circuit utilizing net invariants	20000411	703/16
US 6005315 A	Fault-tolerant magnetic bearing control system architecture	19991221	310/90.5
US 5983168 A	Phase shift accommodation in active vibration damping system	19991109	702/194
US 5956702 A	Time-series trend estimating system and method using column-structured recurrent neural network	19990921	706/22
US 5946481 A	Method for detecting errors in models through restriction	19990831	703/13
US 5925957 A	Fault-tolerant magnetic bearing control system architecture	19990720	310/90.5
US 5911127 A	Prediction of chiller compressor motor overheating	19990608	702/190
US 5901073 A	Method for detecting errors in models through restriction	19990504	703/6
US 5880959 A	Method for computer-aided design of a product or process	19990309	700/97
US 5796922 A	Trainable, state-sampled, network controller	19980818	706/23
US 5771482 A	Estimation of instantaneous indicated torque in multicylinder engines	19980623	701/101
US 5734246 A	Active piezo-electric vibration isolation and directional systems	19980331	318/649
US 5691925 A	Deriving tractable sub-system for model of larger system	19971125	703/2

Number	Title	Date	Class
US 5671336 A	Apparatus based on n-variable unlimited recurrent adjustable network	19970923	706/30
US 5645260 A	Active piezo-electric vibration isolation and directional bracket	19970708	248/550
US 5615137 A	On-the-fly model checking with partial-order state space reduction	19970325	703/17
US 5583792 A	Method and apparatus for integration of traffic measurement and queueing performance evaluation in a network system	19961210	709/224
US 5517594 A	Thermal reactor optimization	19960514	392/416
US 5513299 A	Operation planning system for robot	19960430	700/255
US 5398546 A	Determination of drill bit rate of penetration from surface measurements	19950321	73/152.44
US 5394322 A	Self-tuning controller that extracts process model characteristics	19950228	700/37
US 5325468 A	Operation planning system for robot	19940628	700/262
US 5057992 A	Method and apparatus for controlling or processing operations of varying characteristics	19911015	700/52
US 5014220 A	Reliability model generator	19910507	706/45
JP 07334566 A	ON-THE-FLY MODEL OF SYSTEM FOR EXECUTING CHECKING THROUGH THE USE OF REDUCED PARTIAL ORDER STATE SPACE	19951222	
JP 07141004 A	OPTIMUM FORRESEEN STUDY CONTROLLER	19950602	
JP 01084482 A	RETRIEVING DEVICE FOR INFORMATION TRACK	19890329	
EP 1221663 A2	A method of circuit verification in digital design	20020710	
EP 913781 A2	Static partial order reduction	19990506	
WO 9908268 A1	METHOD OF IMPLEMENTING A LINEAR DISCRETE-TIME STATE-SPACE SERVO CONTROL SYSTEM ON A FIXED-POINT DIGITAL SIGNAL PROCESSOR IN A DISC DRIVE	19990218	
EP 685792 A1	Model checking with state space reduction.	19951206	
NNRD42093	A Method to Optimize Environment Models in Formal Verification	19990401	
NNRD40776	An Iterative Method to Reduce State Space for Efficient Formal Verification	19980301	
NN9108177	Optimistic Parallel Simulation of Continuous Time Markov Chains Using Uniformization.	19910801	
US 20040123254 A	Method for verifying complete model of design system such as integrated circuits, involves restoring subset of abstract variables of abstract model to produce intermediate model using which complete model is verified	20040624	
CA 2355974 A	Generating reduced state space representation for model in compositional state system by converting representations into labelled transition system representation	20030224	
RD 420093 A	Method for optimising environment models in formal verification of computer designs - using state bits of design under test to control input behavioural environment, stressing reactive model under test	19990410	
GB 2342766 B	Disc drive digital signal processor model-based servo control	20020109	

State space reducing iterative method for efficient formal verification - by analysing exact copy of model to remove logic from original after function is re-defined, removing model and re-defining inputs as function of remaining model signals

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RD 407076 A